

10/534462
JC14 Rec'd PCT TO 11 MAY 2005

Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 123886		APPLICATION NO. New U.S. National Stage of PCT/FR2003/003449 10/534,462	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANTS Daniel CHATROUX et al.			
				FILING DATE May 11, 2005			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
/SM/	1.	US 5,703,790	12/30/1997	FARWELL			
/SM/	2.	US 5,486,783	01/23/1996	BAUMERT et al.			
/SM/	3.	US 5,894,508	04/13/1999	KIM			
/SM/	4.	US 6,262,493 B1	07/17/2001	GARNETT			
/SM/	5.	US 6,308,279 B1	10/23/2001	TOLL et al.			
/SM/	6.	US 4,730,268	03/08/1988	MARIN			
/SM/	7.	US 5,877,928	03/02/1999	MAKINO			
/SM/	8.	US 6,229,360 B1	05/08/2001	MIZUNO et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
/SM/	9.	EP 0 638 858 A1	02/15/1995	EUROPE			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
/SM/	10.	S.C. Tan et al.; "Low Power CMOS Level Shifters by Bootstrapping Technique"; Electronic Letters, IEE Stevenage, GB, Vol. 38, No. 16, pages 876-878; August 1, 2002					
EXAMINER				DATE CONSIDERED			
/Suresh Memula/				04/25/2007			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: May 11, 2005